

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.
217755US2S

SERIAL NO.

NEW APPLICATION

PC715 U.S. PT
10/025761
12/26/01

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Tomoaki SHINO

FILING DATE

HEREWITH

GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>Mew</i>	AA	4,809,056	02/28/89	Takehide SHIRATO, et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
<i>Mew</i>	AO	62-76675	04/08/87	JAPAN		X
	AP	05-52672	08/06/93	JAPAN		X
↓	AQ	61-43475	03/03/86	JAPAN		X
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

AW	Kerry BERNSTEIN, et al., "SOI CIRCUIT DESIGN CONCEPTS", SOI Device Structures, 2000, Chap. 2, Sec. 2.4.2, pp 22-23	<input type="checkbox"/> Additional References sheet(s) attached
AX		
AY		
AZ		

Examiner *Matthew E. Warner*Date Considered *10/2/03*

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.